

S. Eder et al.  
U.S. Serial No. 10/618,378  
Page 2 of 12

**Amendments to the specification:**

On page 1, please amend the first line as follows:

~~Description~~ **TITLE OF THE INVENTION**

On page 1, line 5, please add the following:

**FIELD OF THE INVENTION**

On page 1, line 9, please add the following:

**BACKGROUND OF THE INVENTION**

On page 1, line 27 to page 2, line 21, please amend the paragraph as follows:

If data are transmitted between two components in such a system, then every effort is made to output the data on the receiver side at the same output frequency as they have been inputted on the transmitter side. In this case however the following problem arises. FIG. 4 shows the example of the situation, in which data D, that are outputted or inputted by the components A, B in analog form, are transmitted bi-directionally between two components A and B via lines P. In this case the component A uses the frequency  $f_A$  for sending the data D and for outputting received data D. Correspondingly the component  $B$  uses the frequency  $f_B$ . Since in practice the two frequencies  $f_A$  and  $f_B$  are never completely the same, this means that after a certain time more data D is transmitted in one direction than in the other. In FIG. 4 the amount of data D transmitted in both directions is illustrated for clarity over the time  $t$ . Here it

S. Eder et al.  
U.S. Serial No. 10/618,378  
Page 3 of 12

can be seen that seven data elements were transmitted from the component A to the component B, in the opposite direction however only six data elements were transmitted. This means that in the case of the component B in time a data overflow arises, in the case of which more data D are received than outputted, and therefore a data bottleneck develops. In practice this is resolved by the fact that surplus data are eliminated. On the other hand the case arises that the data D are received by the component A at a lower frequency than they are outputted. With the output of the data D therefore data, which are produced by interpolation according to a method known from prior art, are missing.

On page 2, line 22, please add the following:

#### SUMMARY OF THE INVENTION

On page 2, lines 29-32, please amend the paragraph as follows:

According to the invention this objective is achieved by a method ~~with the features of claim 1~~ and a device with the features of claim 14 for the uniform output of asynchronously transmitted digital values. ~~The sub-claims define advantageous and preferential embodiments of the present invention.~~

On page 6, line 30, please add the following:

#### BRIEF DESCRIPTION OF THE DRAWINGS

On page 7, line 15, please add the following:

#### DETAILED DESCRIPTION OF THE INVENTION

S. Eder et al.  
U.S. Serial No. 10/618,378  
Page 4 of 12

Please cancel the Abstract, and add the following new abstract:

A method and device for the uniform output of asynchronously transmitted digital values is provided, including: receiving the digital values in a receiver from a transmission path; outputting the digital values from the receiver on the basis of an output clock for further processing; transmitting the digital values to the transmission path by a transmission device of the receiver; determining the amount of the digital values received by the receiver in relation to the time; adjusting the output clock on the basis of the determined amount in such a way that the digital values are outputted at the frequency with which on time average the receiver receives the digital values; and adjusting a transmission clock of the transmission device to correspond to the output clock of the receiver.